

APPLICATION DATA SHEET**Inventor Information**

Inventor One Given Name : Howard C.
 Family Name : Kirsch
 Postal Address Line One : 479 West Bogus View Drive
 City : Eagle
 State or Province : Idaho
 County : Ada
 Postal or Zip Code : 83616
 City of Residence : Eagle
 State or Prov. Of Residence : Idaho
 Country of Residence : United States of America
 Citizenship Country : United States of America

Correspondence Information

Name Line One : Kimton N. Eng, Esq.
 Address Line One : DORSEY & WHITNEY LLP
 Address Line Two : 1420 Fifth Avenue, Suite 3400
 City : Seattle
 State or Province : Washington
 Postal or Zip Code : 98101
 Telephone : 206-903-8800
 Facsimile : 206-903-8820
 E-Mail : patentseattle@dorsey.com

Application Information

Title : SYSTEM AND METHOD TO AVOID VOLTAGE READ
 ERRORS IN OPEN DIGIT LINE ARRAY DYNAMIC
 RANDOM ACCESS MEMORIES
 Total Drawing Sheets : 6
 Formal Drawings : YES
 Application Type : Utility
 Attorney Docket Number : 501214.02 (30204/US/2)
 Assigned : Yes (Large Entity)

Representative Information

Representative Customer No. : 27,076

Continuity Information

This application is a : continuation of
 >Application One : 10/231,680
 Filing Date : August 29, 2002
 h:\ip\documents\clients\micron technology\1200\501214.02\501214.02 application data sheet.doc